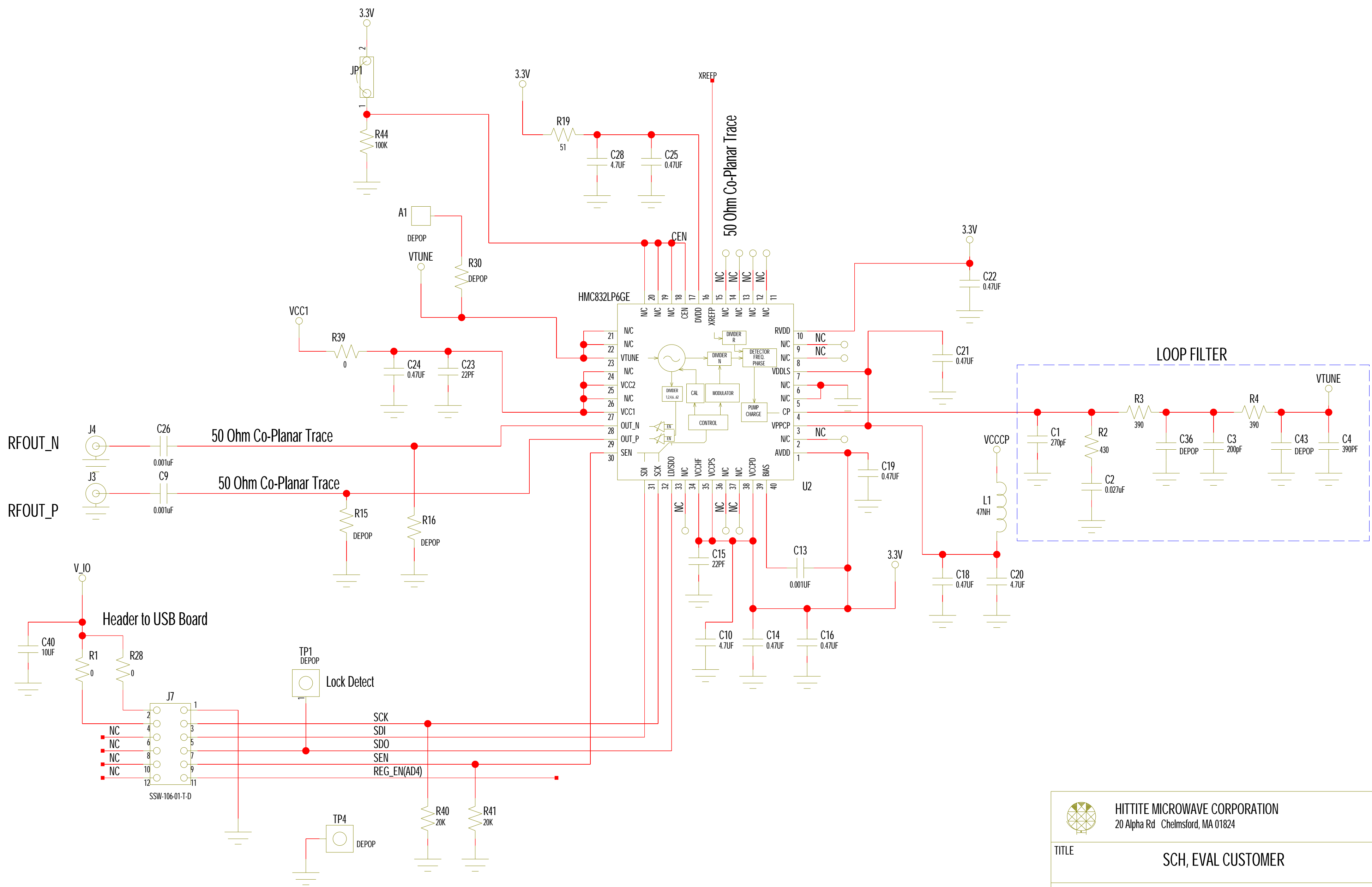



REVISIONS					
REV	ECN#	ZONE	DESCRIPTION	NAME	DATE
B	CP120985	----	CHANGE AS PER CP120985	D. ACEVAL	07/10/2012
C	CP121326	----	CHANGE AS PER CP121326	D. ACEVAL	10/12/2012

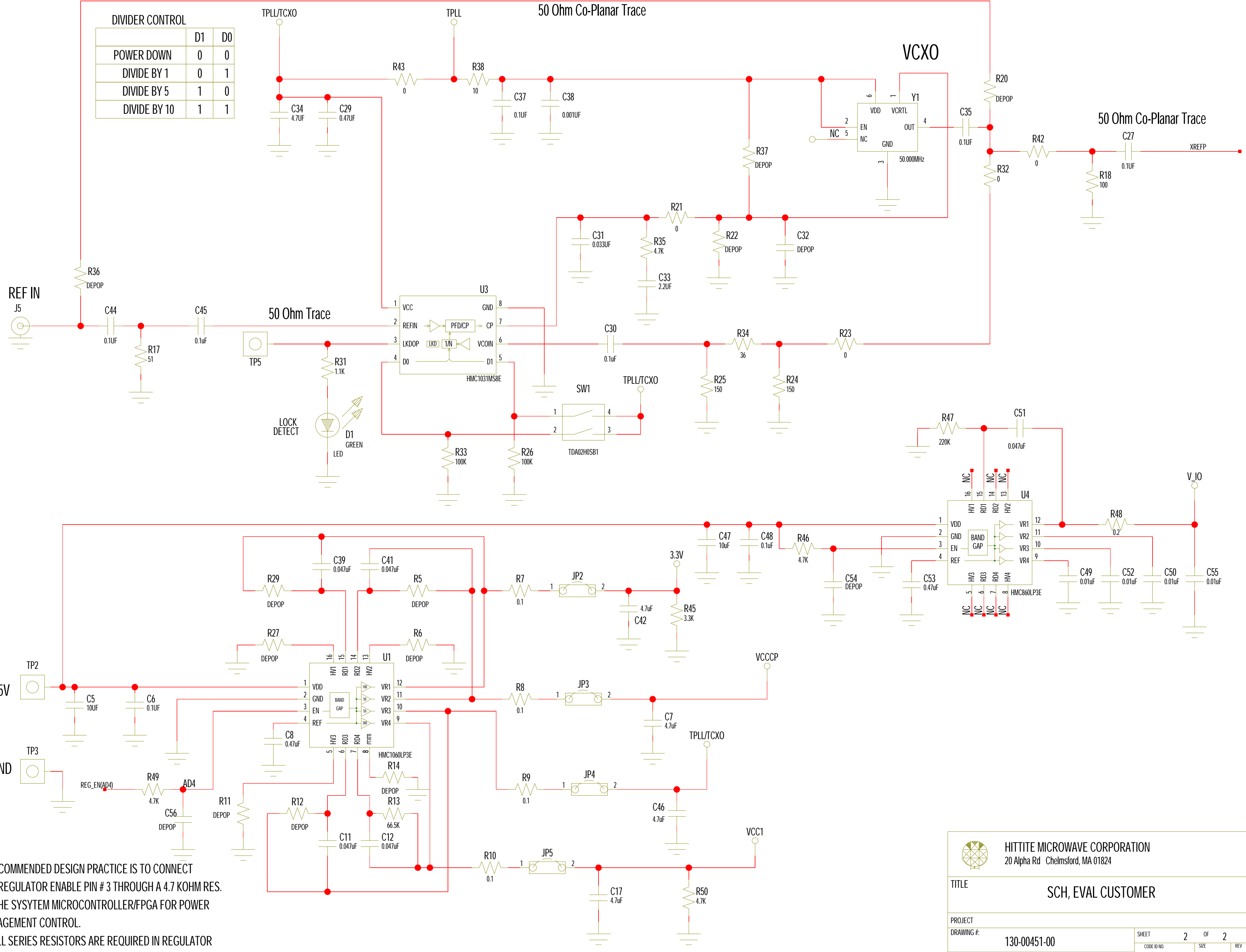


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 <b>HITTITE MICROWAVE CORPORATION</b> 20 Alpha Rd Chelmsford, MA 01824		TITLE	
		SCH, EVAL CUSTOMER	
PROJECT		SHEET 1 OF 2	
DRAWING #: 130-00451-00		CODE ID NO. 1CN88	SIZE C
DRAWN BY: D. YOUNG	DATE: 11/18/10	REV: C	REV: C

12-10-2012\_14:08

DIVIDER CONTROL		
	D1	D0
POWER DOWN	0	0
DIVIDE BY 1	0	1
DIVIDE BY 5	1	0
DIVIDE BY 10	1	1



A RECOMMENDED DESIGN PRACTICE IS TO CONNECT THE REGULATOR ENABLE PIN # 3 THROUGH A 4.7 KOHM RES. TO THE SYSYEM MICROCONTROLLER/FPGA FOR POWER MANAGEMENT CONTROL. SMALL SERIES RESISTORS ARE REQUIRED IN REGULATOR OUTPUT PATHS AS SHOWN

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D. ACEVAL		11/18/10	
SHEET		OF	
2		2	
CODE ID NO.		SIZE	
1CN88		C	
REV		C	